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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

:

SAMUEL BOUTIN, ET AL.

: EXAMINER: PATEL, S.K.

SERIAL NO: 10/532,089

:

FILED: FEBRUARY 21, 2006

: GROUP ART UNIT: 2128

FOR: METHOD AND DEVICE FOR
SYNTHESISING AN ELECTRICAL
ARCHITECTURE

:

DO NOT ENTER: /S.P./

AMENDMENT UNDER 37 C.F.R. § 1.116

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated July 1, 2009, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 13 of this paper.